

Fig. 1

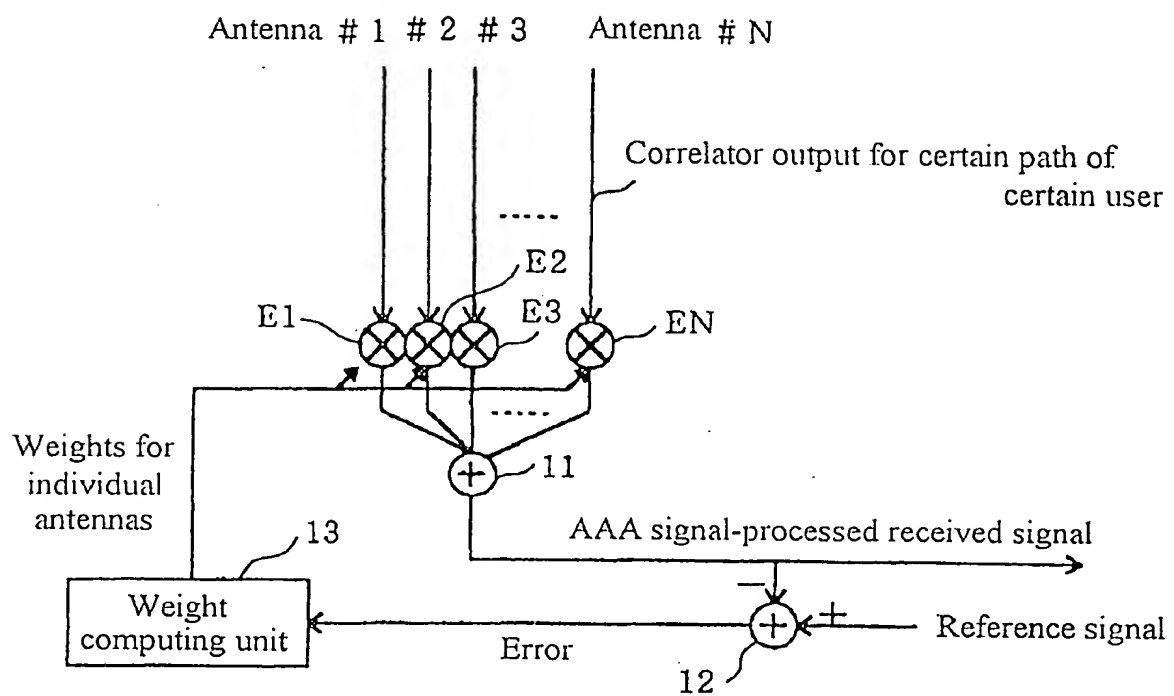
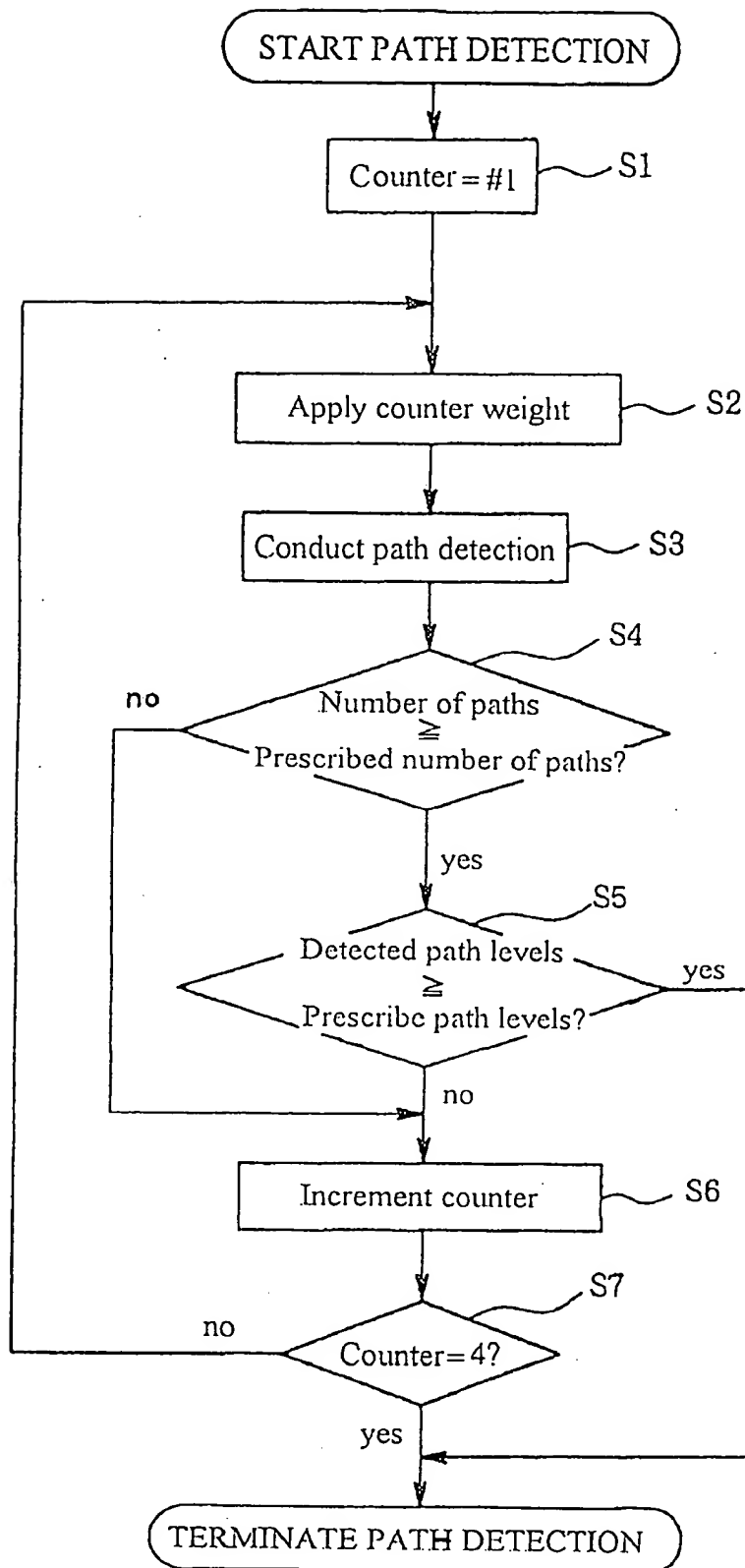


Fig. 2

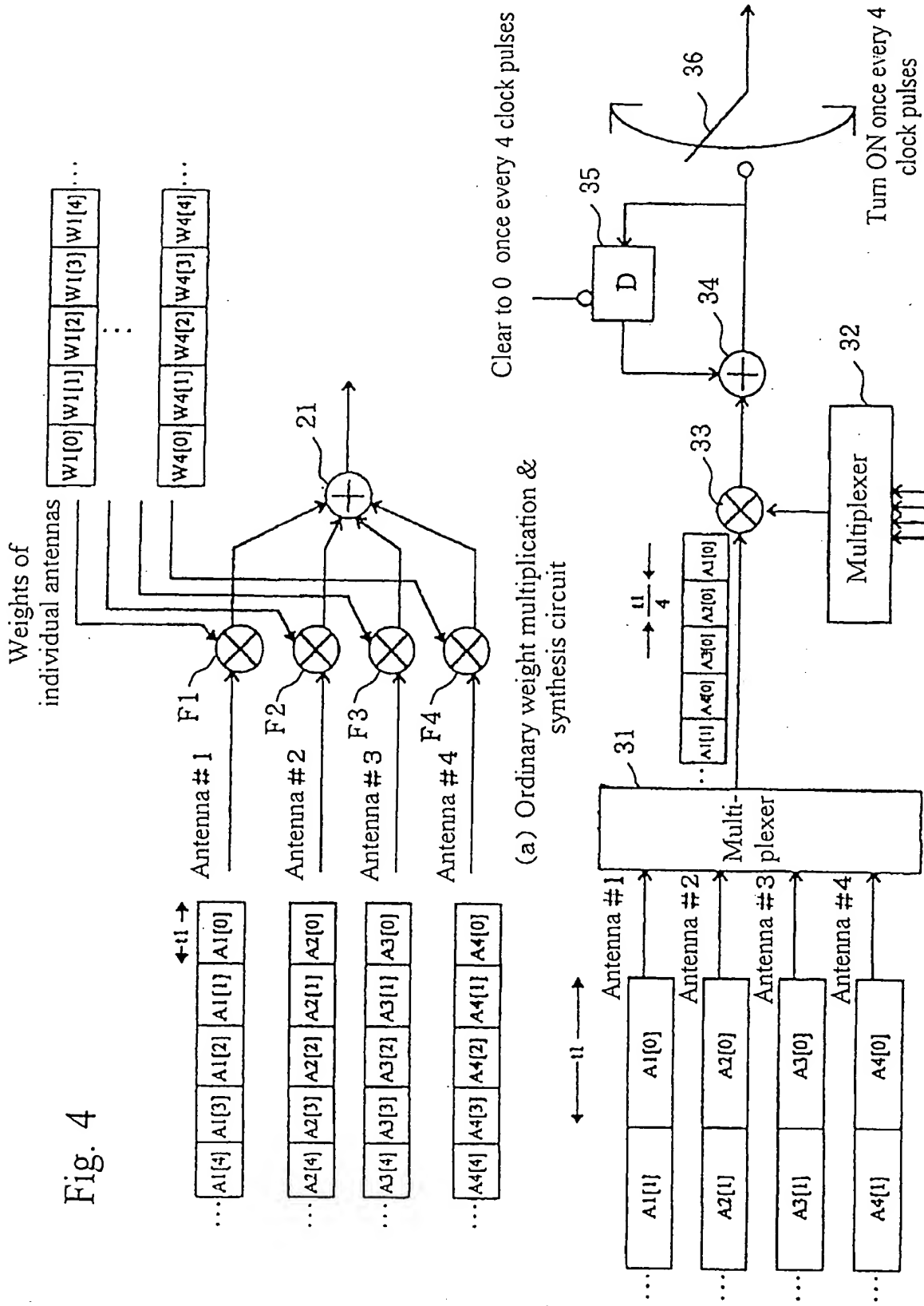


#1 : Leading path weight
 #2 : Highest path weight
 #3 : Average path weight
 #4 : Lowest path weight

Terminate path detection if
 number of paths and path levels
 sufficient

Fig. 3

Fig. 4



(a) Ordinary weight multiplication & synthesis circuit

(b) Weight multiplication & synthesis circuit using time division

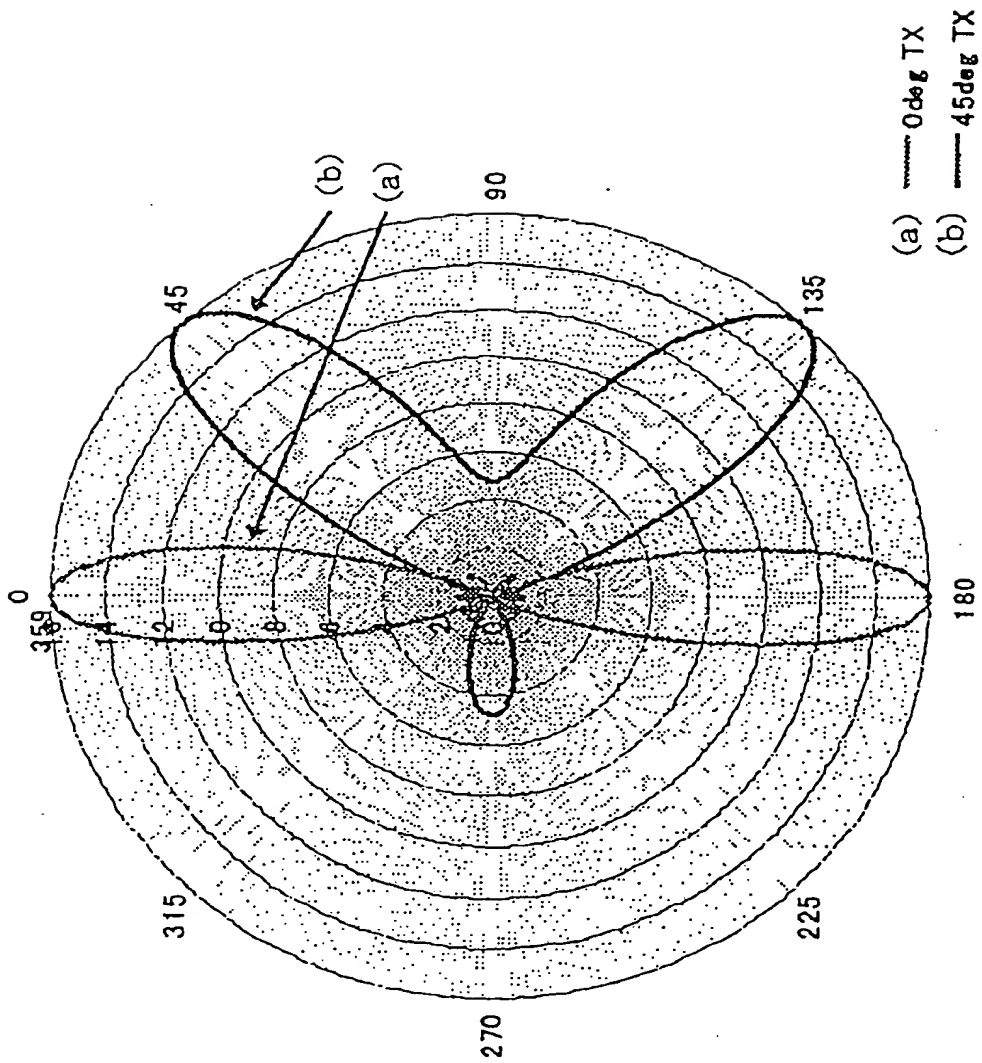


Fig. 5

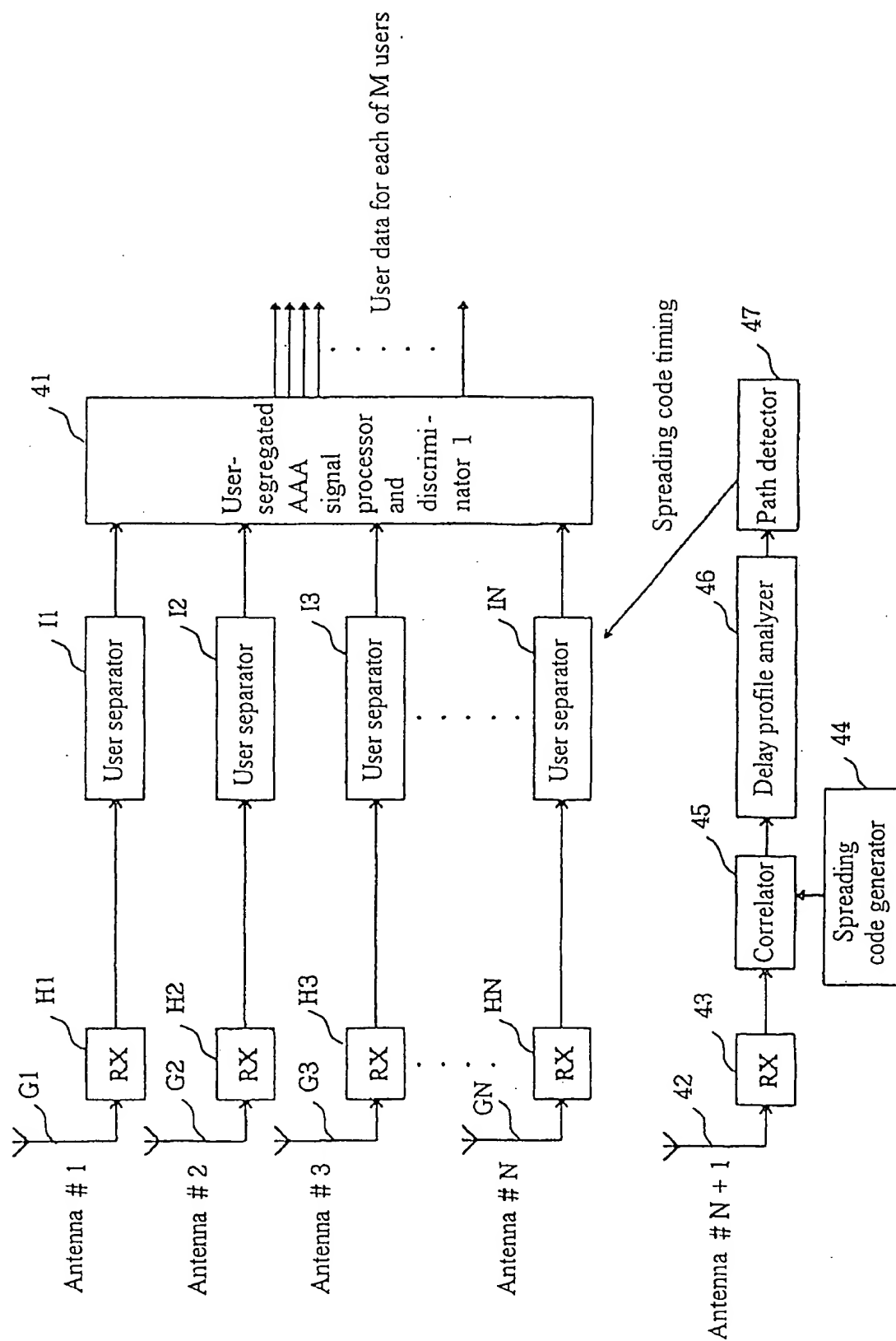


Fig. 6

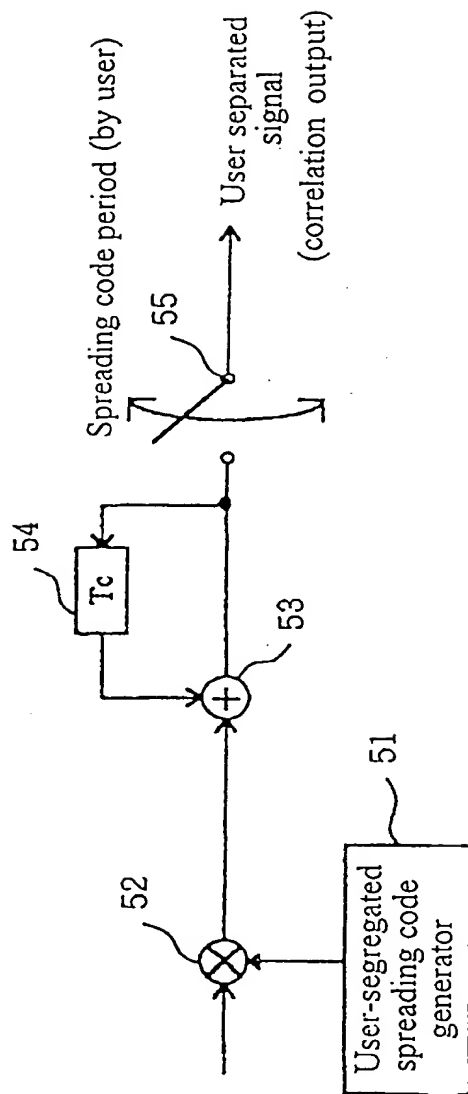


Fig. 7

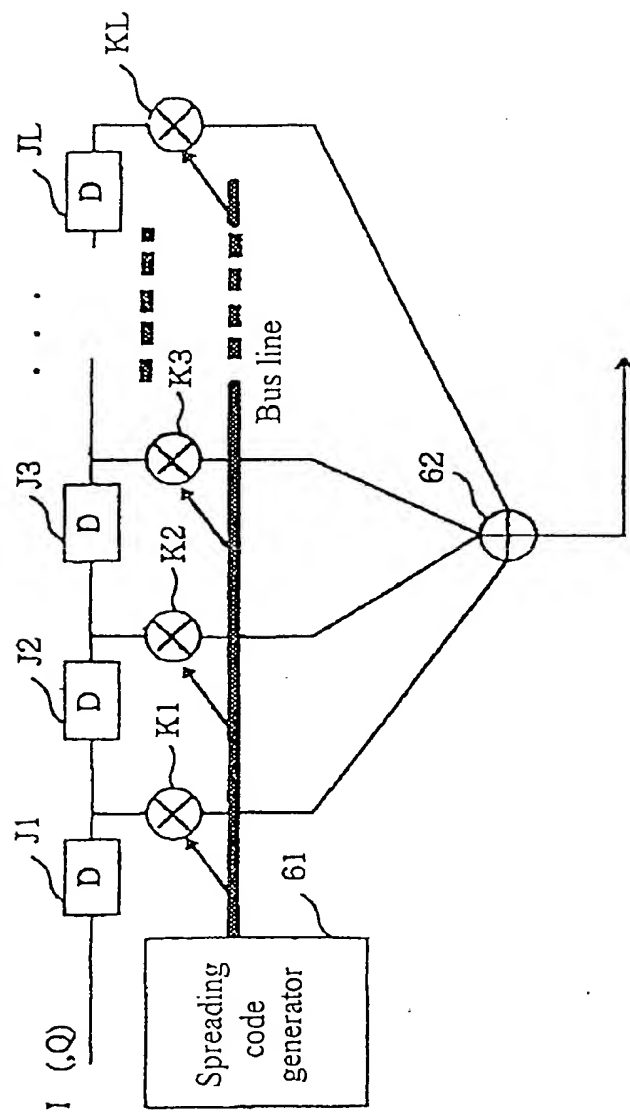


Fig. 8

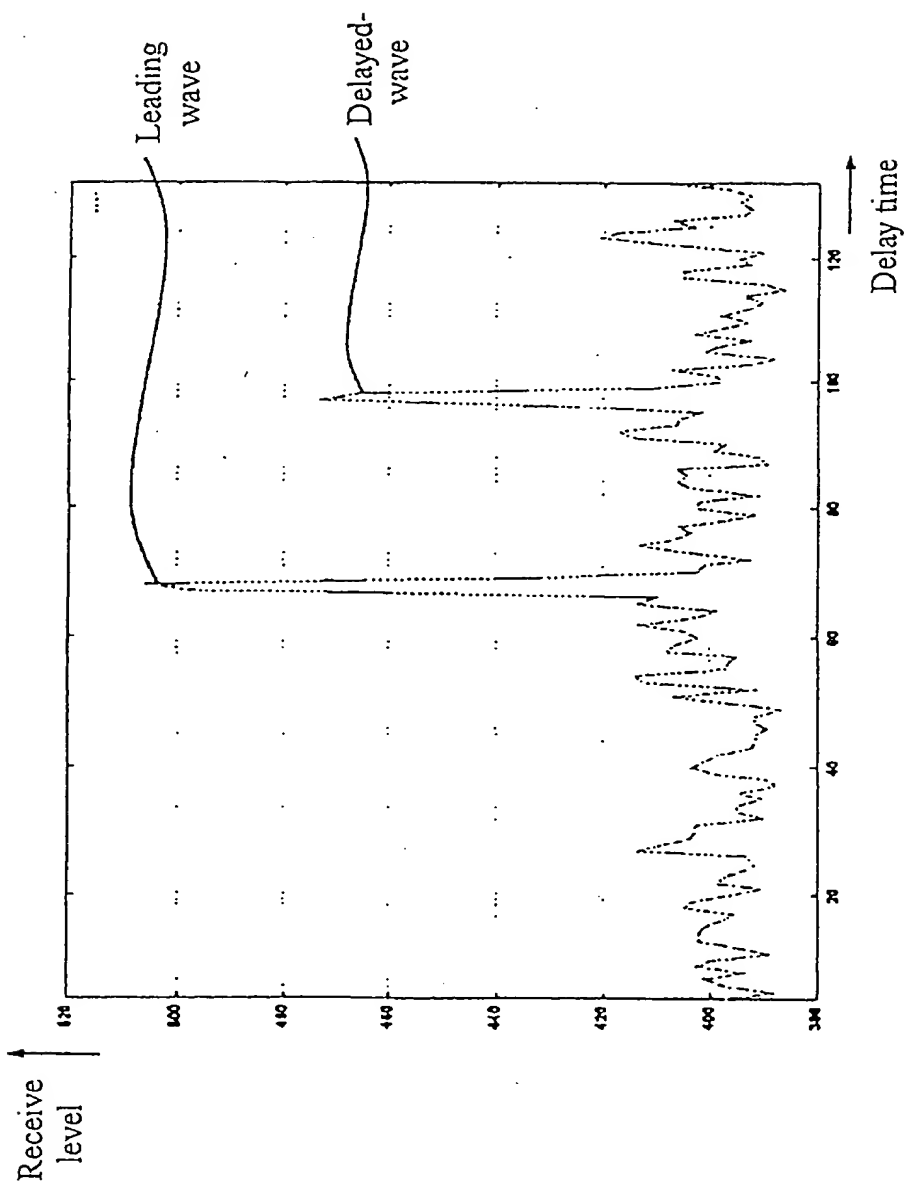


Fig. 9

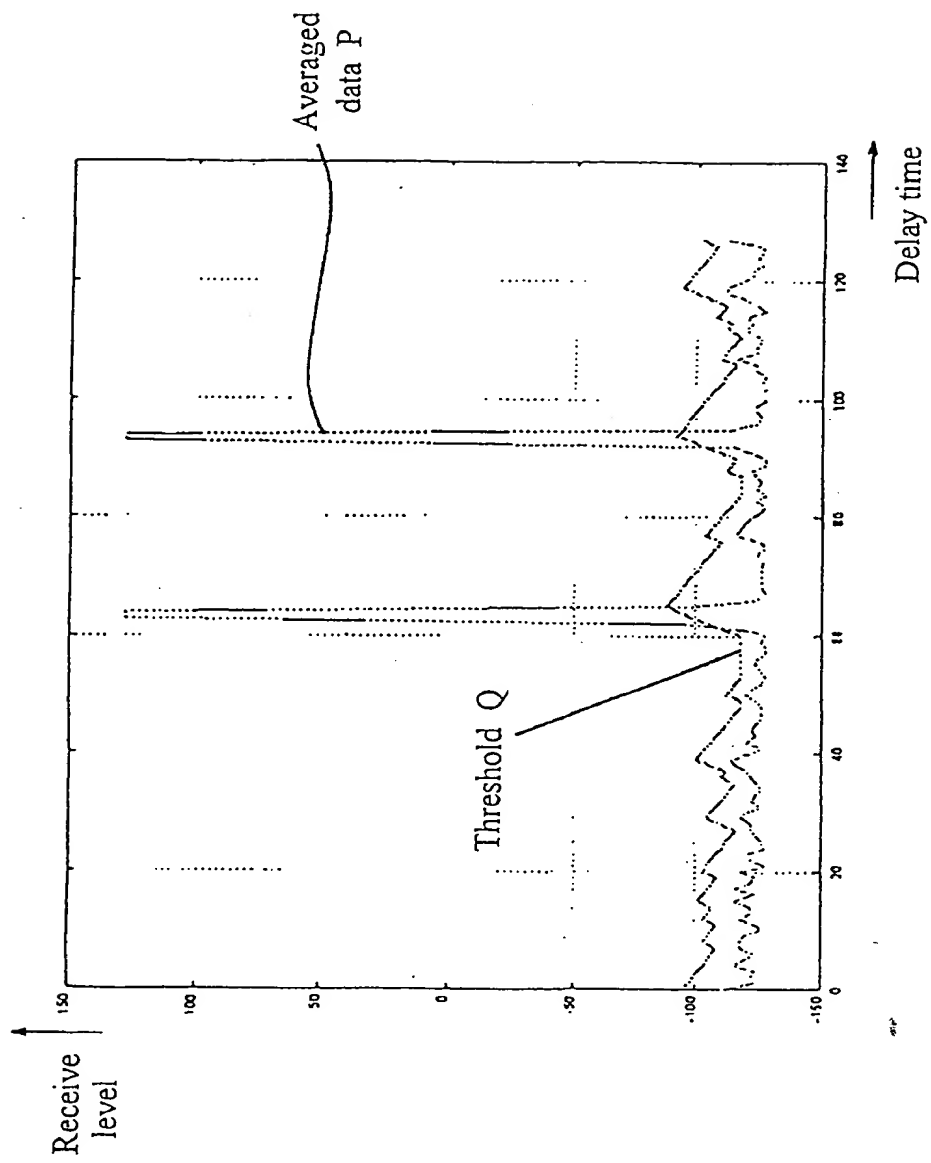


Fig. 10